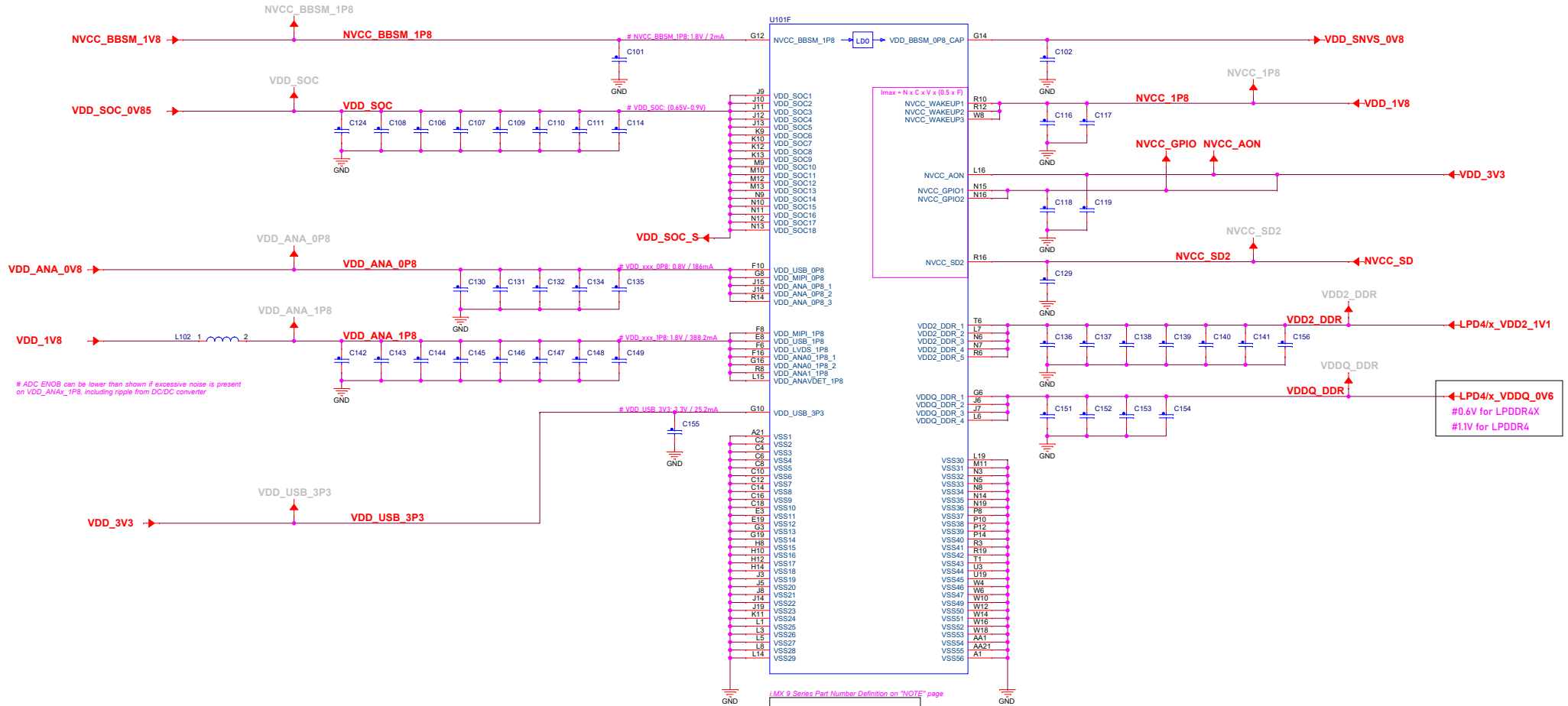


i.MX93 PWR



i.MX 9 Series Part Number Definition on "NOTE" page

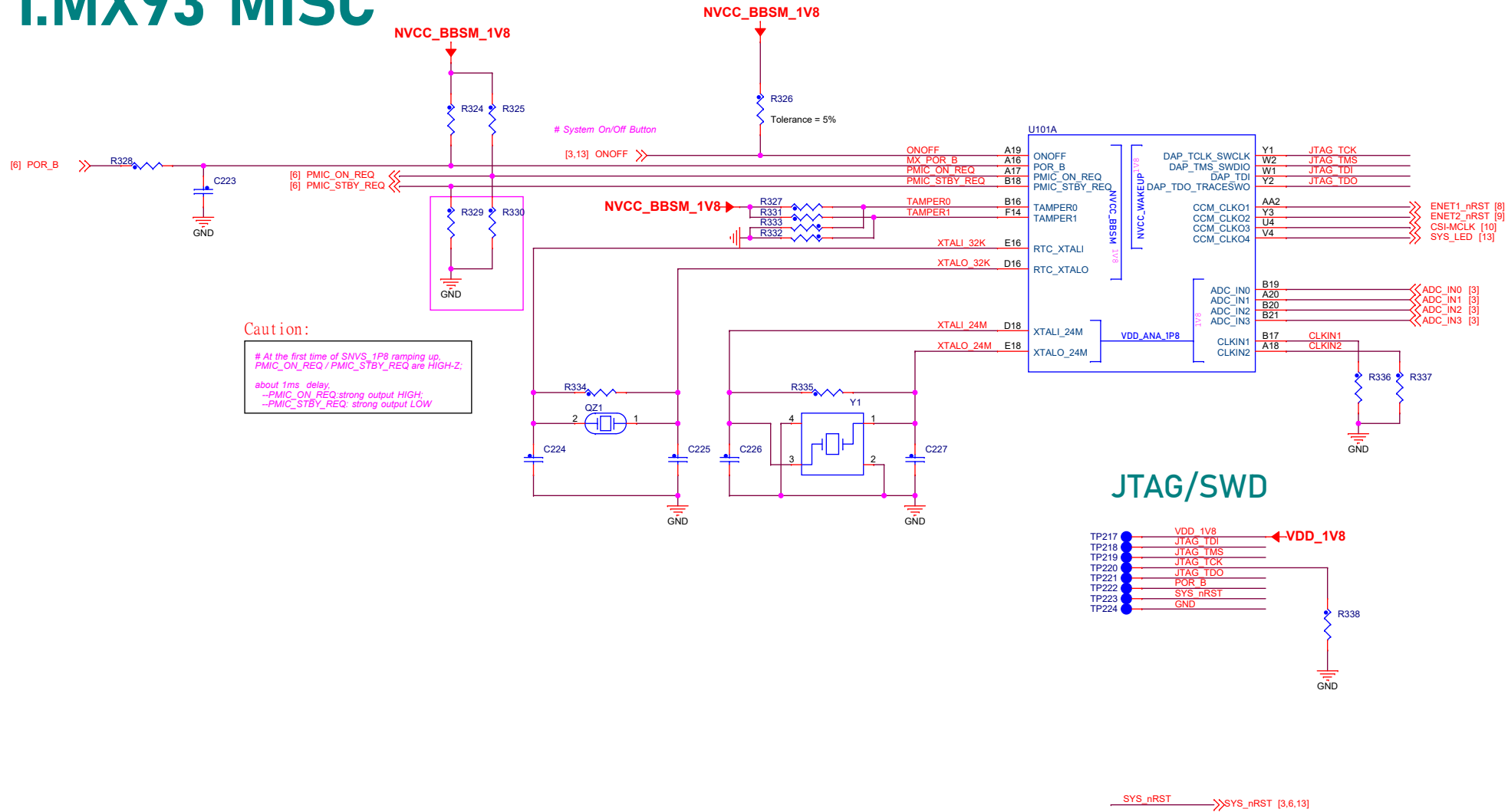
Pre-production A0: PIMX9332CVUJMAA
Full featured A0: MIMX9332CVUJMAA
Full featured A1: MIMX9332CVUJMAA

NOTE:

All the CAPs in this page need to be put on the bottom of BGA, and close the PINs as short as possible.

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Designer: HW	Drawing Title: DEBIX Model C Schematics
Reviewer: HW	Page Title: PAGE01 CPU PWR
Date:	Sheet 1 of 13 Rev V02

i.MX93 MISC



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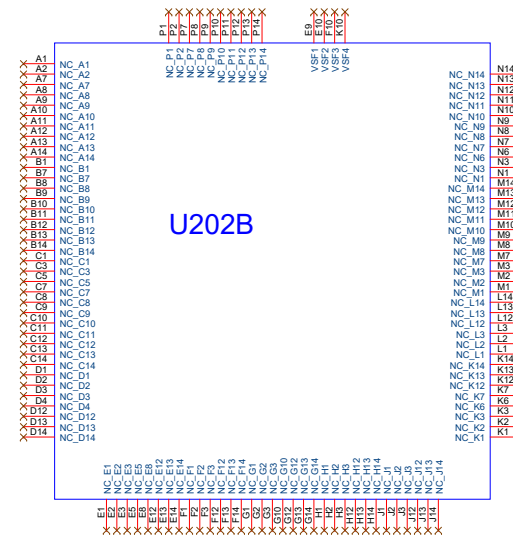
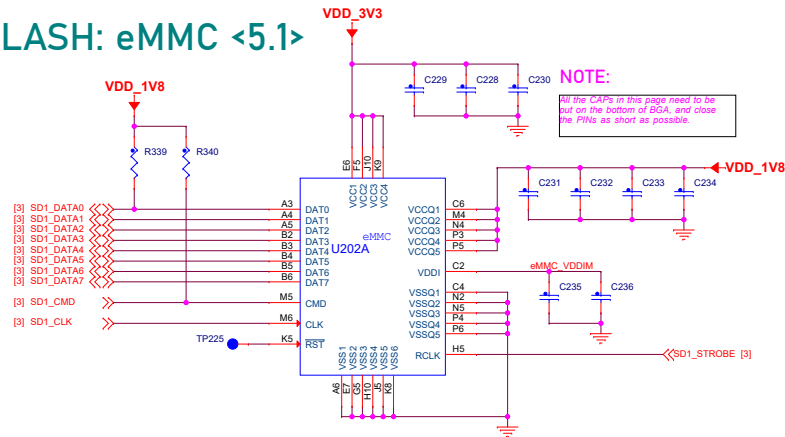
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Designer: HW Drawing Title: **DEBIX Model C Schematics**

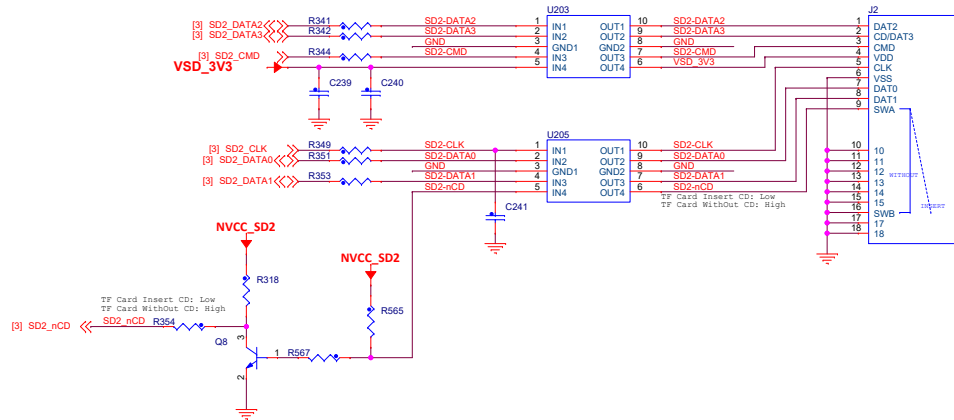
Reviewer: HW Page Title: **PAGE04 CPU MISC**

Date: Sheet 4 of 13 Rev V02

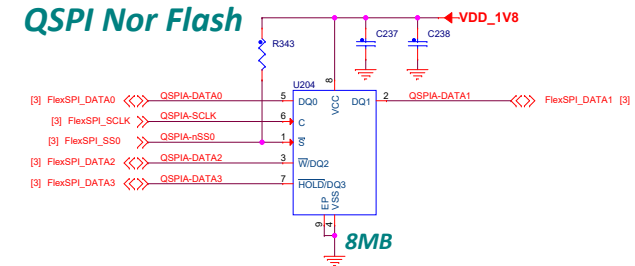
FLASH: eMMC <5.1>



Micro SD Card Slot

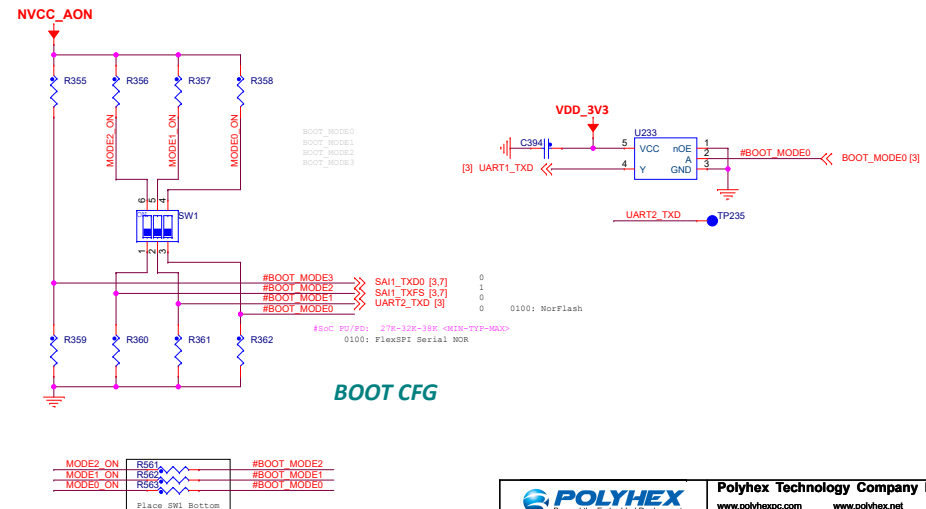


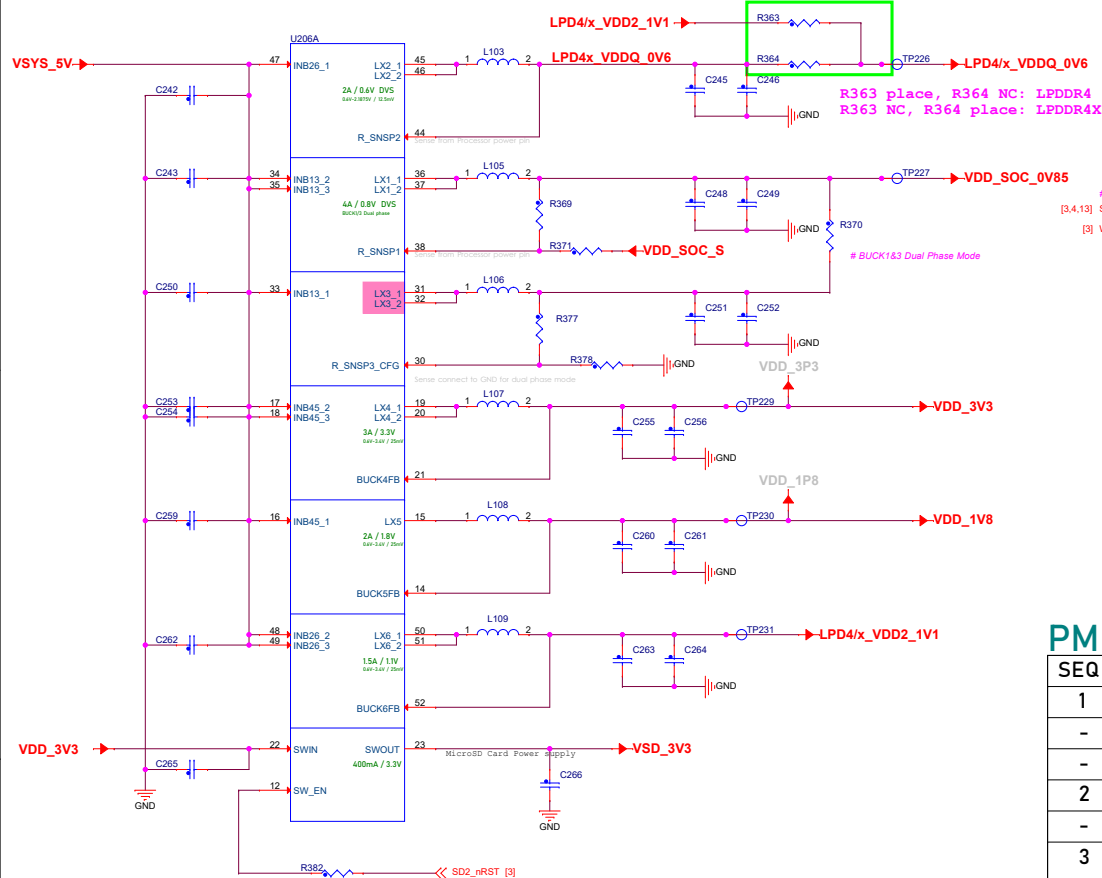
QSPI Nor Flash



i.MX93 BOOT MODE (Boot Mode and CFG Switch)

BOOT_MODE[3:0]	BOOT CORE	BOOT DEVICE	COMMENT
0000	Cortex-A55	From internal fuses	
0001	Cortex-A55	Serial Downloader	USB1/2
0010	Cortex-A55	USDHC1 8-bit eMMC 5.1	
0011	Cortex-A55	USDHC2 4-bit SD3.0	
0100	Cortex-A55	FlexSPI Serial NOR	with SFDP (JESD-216) discoverable parameters
0101	Cortex-A55	FlexSPI Serial NAND 2K page	
0110	Cortex-A55	Infinite Loop	
0111	Cortex-A55	Test Mode	
1000	Cortex-M33	From internal fuses	
1001	Cortex-M33	Serial Downloader	USB1
1010	Cortex-M33	USDHC1 8-bit eMMC 5.1	
1011	Cortex-M33	USDHC2 4-bit SD3.0	
1100	Cortex-M33	FlexSPI Serial NOR	with SFDP (JESD-216) discoverable parameters
1101	Cortex-M33	FlexSPI Serial NAND 2K page	
1110	Cortex-M33	Infinite Loop	
1111	Cortex-M33	Test Mode	





System Reset Button
[3,4,13] SYS_nRST
[3] WDOG_B

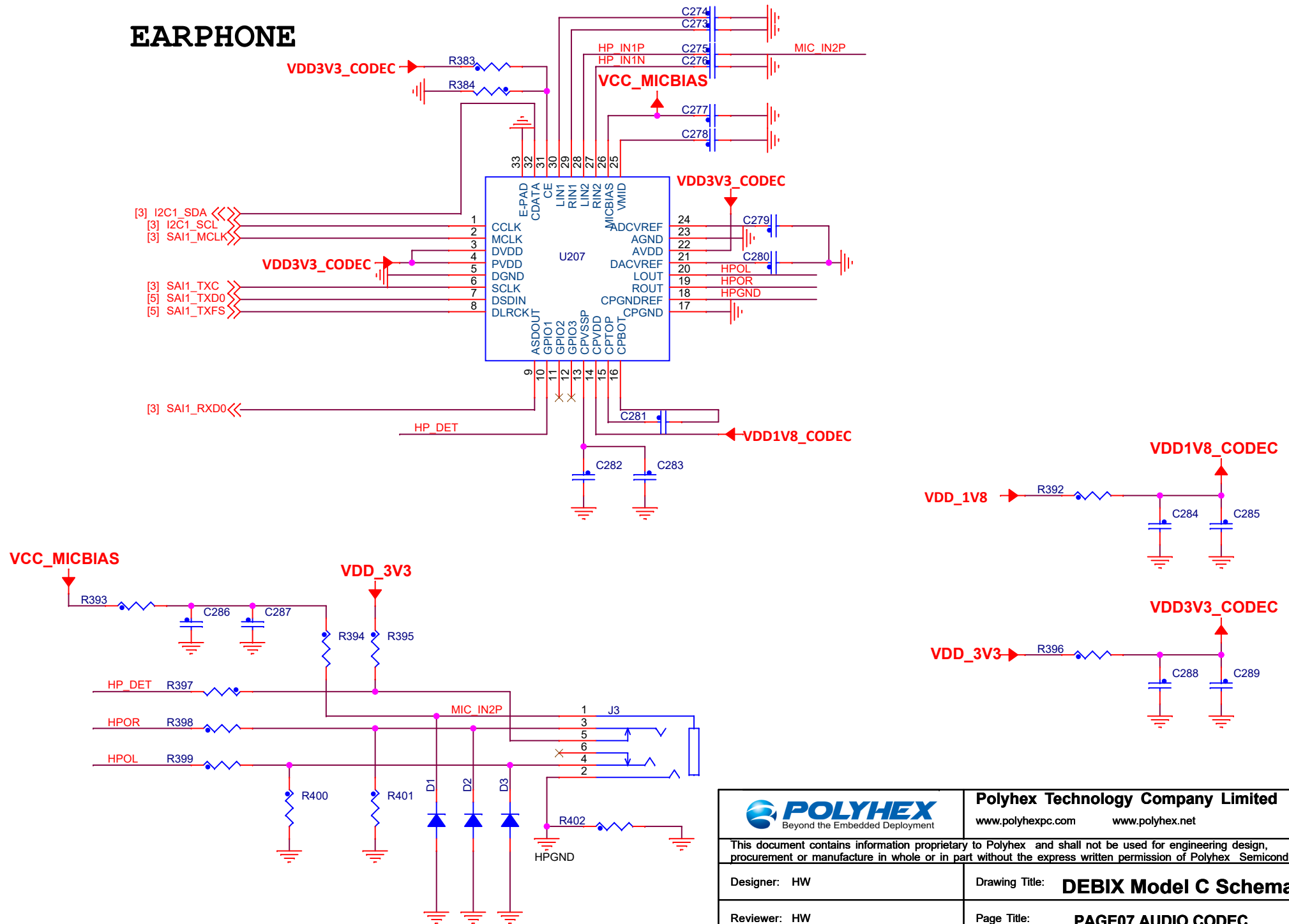
[4] PMIC_ON_REQ
[4] PMIC_STBY_REQ
[3] I2C2_SCL
[3] I2C2_SDA

PMIC: PCA9451A CFG

SEQ	tstep=2ms	REGULATOR	VOL (V)	MAX I (mA)	PWR RAIL	TYP VOL(V)	REQ I (mA)
1		LD01	1.8	10	NVCC_BBSM_1V8	1.8	2
-					PMIC_ON_REQ		
2	T1	BUCK1/3 DP	0.85	4000	VDD_SOC	DVS	Ref to DS
-					VDD_ANA_0P8 VDD_MIPI_0P8 VDD_USB_0P8	0.8	186
3	T2	LD04	0.8	200			
4							
5	T4	BUCK5	1.8	2000	VDD_ANAx_1P8 VDD_LVDS_1P8 VDD_MIPI_1P8 VDD_USB_1P8 NVCC_GPIO	1.8	1040 PERI INCLUDE
6	T5	BUCK6	1.1	1500	VDD2_DDR VDDQ_DDR (1.1V for LPD4)	1.1	676
7	T6	BUCK2	0.6	2000	VDDQ_DDR (0.6V for LPD4x)	0.6	360
8	T7	BUCK4	3.3	3000	NVCC_GPIO VDD_USB_3P3	3.3	2870 PERI INCLUDE
8	T7	Load Switch	-	400	SD_CARD (from BUCK4)	3.3	
9	T8	LD05	1.8/3.3	150	NVCC_SD2	1.8/3.3	
-							
12		POR_B	--	--	POR_B		

Note:
PIN2/54 is NC. C728/C729 just keep the compatible with previous design.

EARPHONE

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Designer: HW

Drawing Title:

DEBIX Model C Schematics

Reviewer: HW

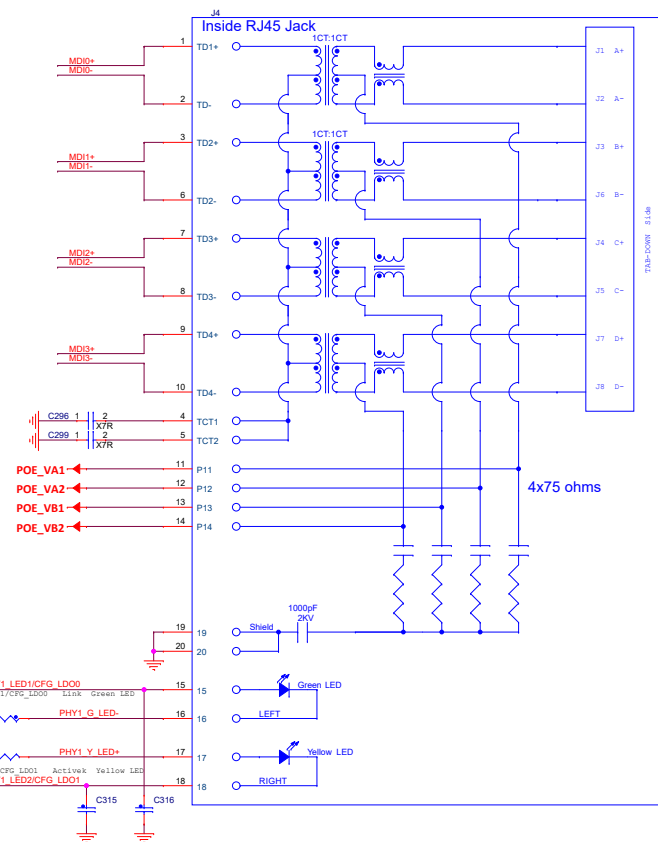
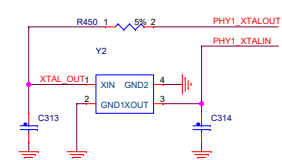
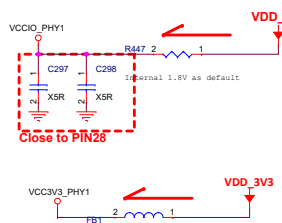
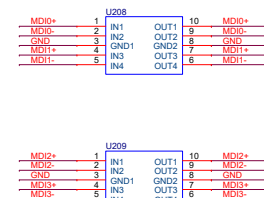
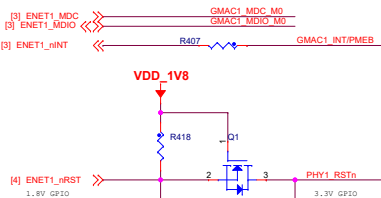
Page Title:

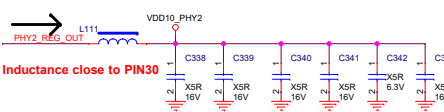
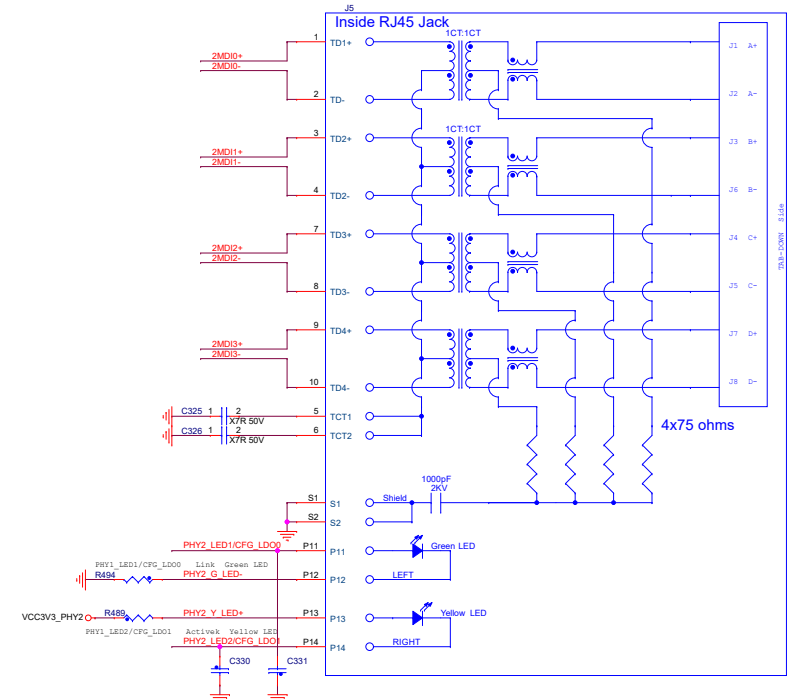
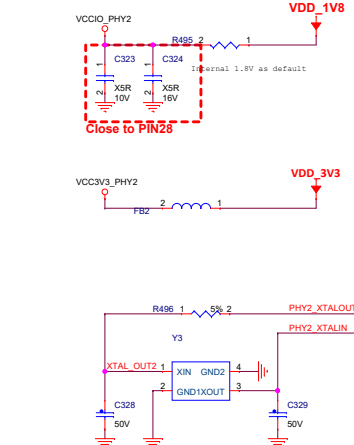
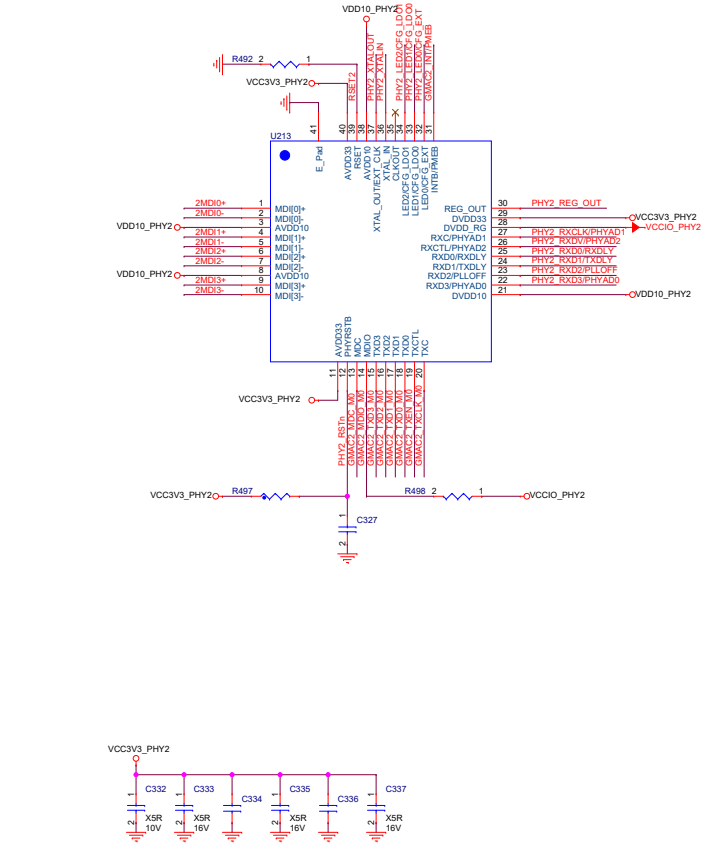
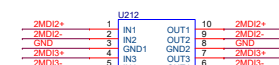
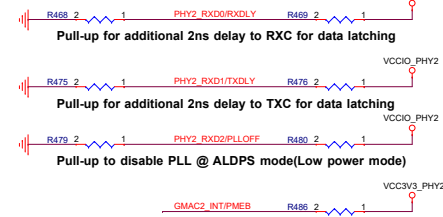
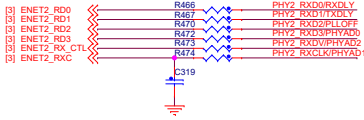
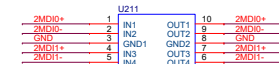
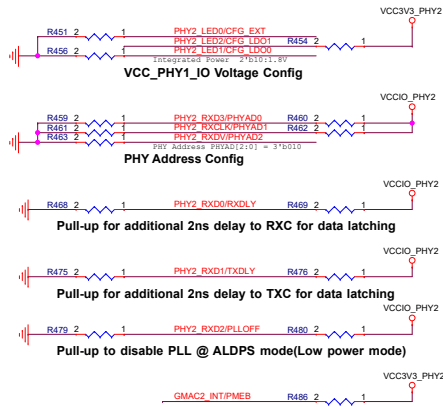
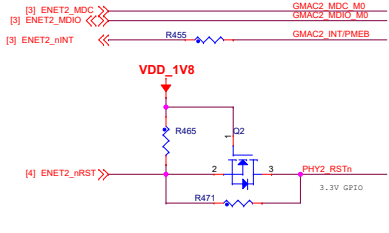
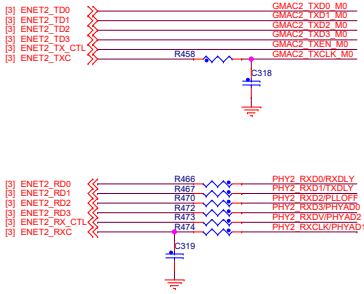
PAGE07 AUDIO CODEC

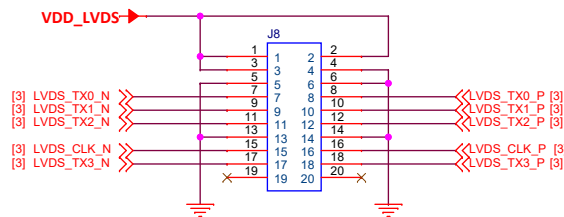
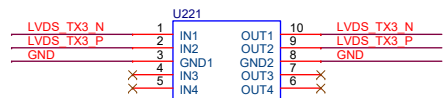
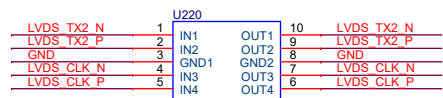
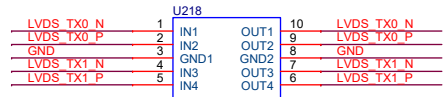
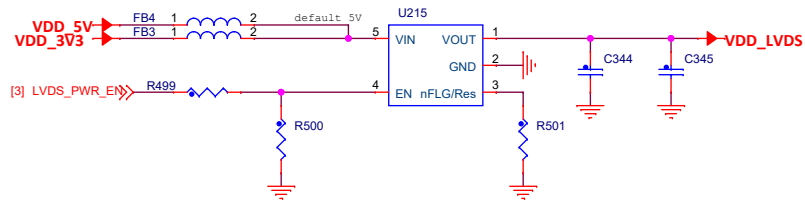
Date:

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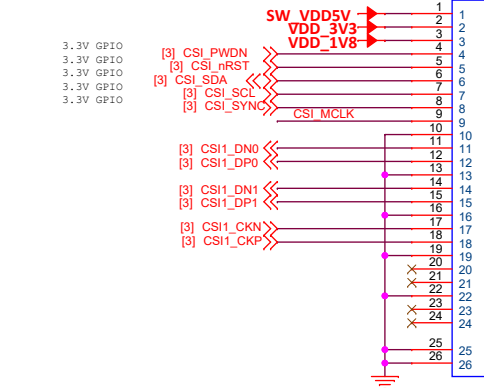
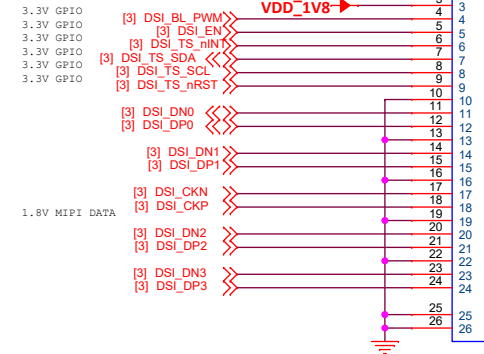
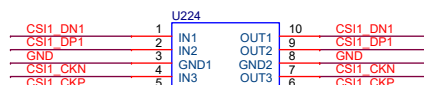
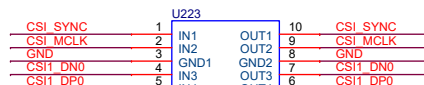
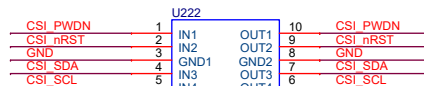
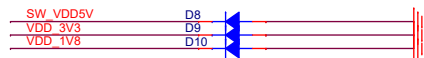
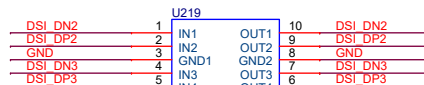
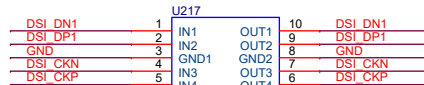
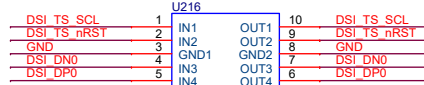
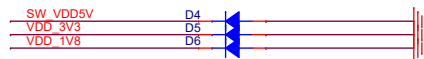
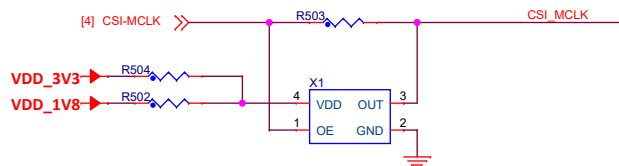
Rev V02

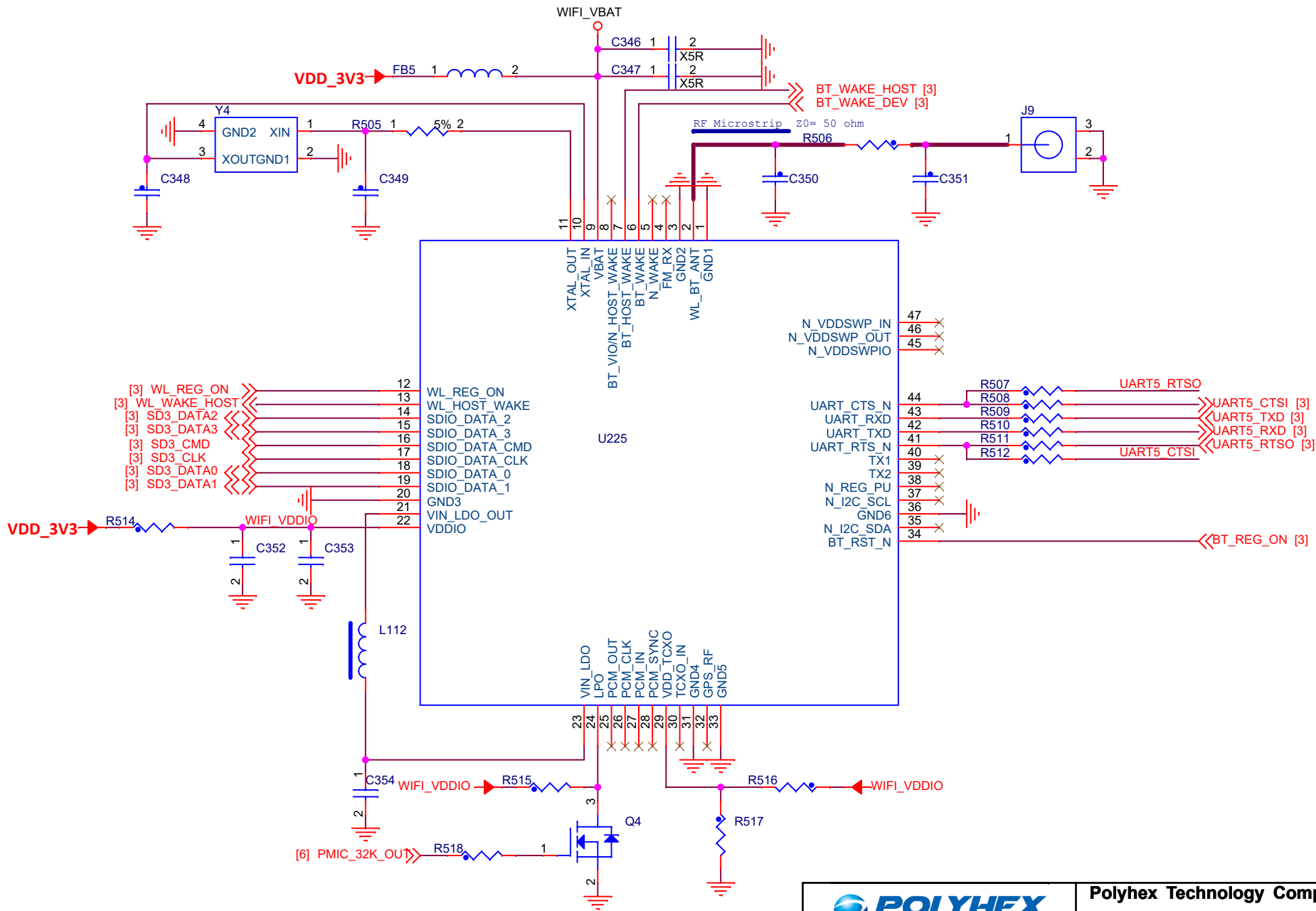






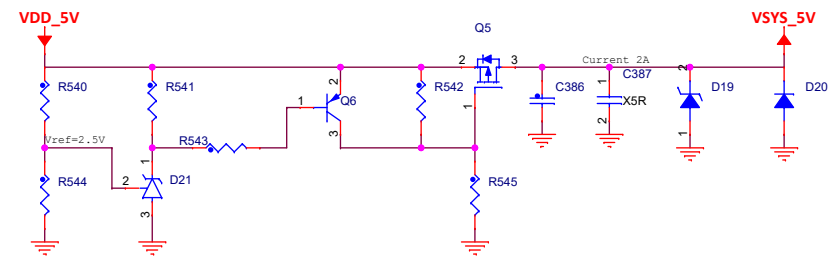
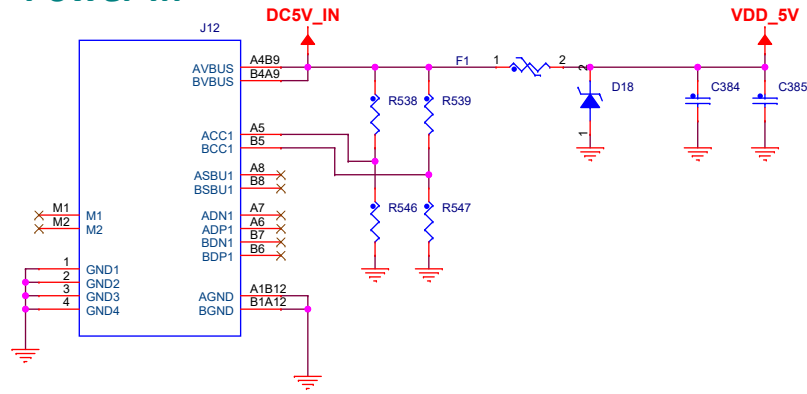
LVDS DATA 1.8V



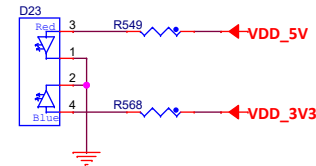


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Reviewer: HW	Page Title: PAGE11 WIFI&BT
Date:	Sheet 11 of 13 Rev V02

Power In



Power LED



System LED

